

Research Article

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High-detectivity tin disulfide nanowire photodetectors with manipulation of localized ferroelectric polarization field

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Abstract: Tin sulfide semiconductor nanowires (NWs) have been widely investigated for photodetection applications because of their good optical and electrical properties. Herein, we synthesized n-type SnS_2 NWs and then fabricated SnS_2 NW photodetectors with a ferroelectric polymer side-gate. The strong electric field induced by ferroelectric polymer can effectively suppress the dark current and improve the detectivity in SnS_2 NW photodetectors. The photodetectors after polarization depletion exhibit a high

photoconductive gain of 4.0×10^5 and a high responsivity of $2.1 \times 10^5 \text{ A W}^{-1}$. Compared with devices without polarization depletion, the detectivity of polarization-depleted photodetectors is improved by at least two orders of magnitude, and the highest detectivity is 1.3×10^{16} Jones. Further, the rise and fall time are 56 and 91 ms respectively, which are about tens of times faster than those without polarization depletion. The device also shows a good spectral response from ultraviolet to near-infrared. This study demonstrates that ferroelectric materials can enhance optoelectronic properties of low-dimensional semiconductors for high-performance photodetectors.

Keywords: ferroelectric; nanowire; photodetector; SnS_2 .

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1 Introduction

One-dimensional semiconductor NWs have been intensively studied for room-temperature photodetection with high sensitivity, gain and wide spectral response, etc. [1–6]. IV–VI chalcogenide tin sulfide semiconductors, including SnS [7–10] and SnS_2 [11–14] shows tremendous potential in optoelectronic applications because of their narrow bandgap, high carrier mobility and light–matter coupling efficiency, etc. [9, 11]. For example, p-type SnS NWs have been synthesized and demonstrated excellent optoelectronic performance in our previous work [7]. SnS_2 is an n-type semiconductor with bandgap ranging from 2.1 to 2.4 eV [11, 15], and SnS_2 -based nanostructures have been demonstrated to show broad prospects in field-effect transistors [14, 15], battery materials [16, 17], photodetectors [12, 13, 18], gas sensors [19], and solar cells [20, 21]. Most studies on SnS_2 nanostructures mainly focus on quantum dots [22], nanosheets [12, 13, 18] and thin films [23, 24], but very limited study on SnS_2 NWs. Thus, it is essential to explore 1D SnS_2 NWs and their optoelectronic properties. In addition, whether it is an n-type or p-type NW photodetector, its optoelectronic performances have been greatly

improved compared with traditional photodetectors. However, the large dark current is still a major problem to be solved, and it is necessary to improve these problems by designing a certain device structure or changing certain conditions of the photodetector.

In this work, we synthesized SnS₂ NWs by the CVD method. To suppress the dark current caused by defect/trap induced carriers and improve the detectivity of photodetectors, we integrated ferroelectric polymer side-gate with SnS₂ NW. The intrinsic carriers in the SnS₂ NW channel can be depleted by ultrahigh electrostatic field induced by the polarization of ferroelectric polymer. Therefore, the background current is suppressed to as low as 10^{-12} A. Furthermore, the detectivity is increased by at least two orders of magnitude, and the rise and fall time are improved by tens of times. The noise characteristics show that the device has low noise equivalent power (NEP) and noise current (the frequency noise current power is $\sim 6.9 \times 10^{-28}$ A² and $1/f$ noise current is $\sim 2.6 \times 10^{-14}$ A, respectively). The SnS₂ NW photodetectors after polarization depletion exhibit a high photoconductive gain of 4.0×10^5 , a high responsivity of 2.1×10^5 A W⁻¹ and ultra-high detectivity of 1.3×10^{16} Jones.

2 Experimental section

2.1 Nanowires synthesis and characterization

In this work, the SnS₂ NWs were synthesized in a tube furnace via the CVD method. The high-purity SnS₂ powder (0.1 g) was put at the heating zone center as source material and S powder (0.1 g) was put upstream of the central temperature zone to the SnS₂ powder at a distance of ~ 15 cm. The Au film with a thickness of 1 nm was pre-deposited onto the Si substrate as the catalyst. The substrate was put downstream approximately 12 cm away from the SnS₂ powder. A carrier gas mixture of argon and hydrogen (10%) of 100 sccm was introduced into the tube. Then the temperature of the SnS₂ powder was heated rapidly to 760 °C and kept at this temperature for 30 min. After that, the furnace was cooled naturally to room temperature under carrier gas flow. Scanning electron microscope (SEM), X-ray diffraction (XRD), high-resolution transmission electron microscopy (HR-TEM), and energy-dispersive X-ray spectroscopy (EDS) characterizations were performed to determine the crystal properties, diameter, elemental composition of as-synthesized SnS₂ NWs together.

2.2 Photodetectors fabrication and characterization

The field-effect transistors (FETs) on the basis of single SnS₂ NW can be assembled by electron-beam lithography (EBL). The SnS₂ NWs were first dispersed by contact and physically transferred onto Si/SiO₂ substrate with marks, following by spin-coating with MMA and PMMA. EBL with an NPGS system (JEOL 6510) was used to determine the drain and source electrodes. Before metallization, the SnS₂ NWs were

immersed in diluted HF (2%) solution for 15 s to eliminate the surface oxide layer. The Cr/Au (15 nm/60 nm) electrodes were completed through vacuum thermal evaporation and stripping. A ferroelectric polymer poly(vinylidene fluoride-trifluoroethylene) [P(VDF-TrFE)] with 300 nm in thickness was placed on the SnS₂ NW devices through the spin-coating method. Then the P(VDF-TrFE) film was annealed at 150 °C for 3 h to enhance its crystallinity. The electrical and optoelectronic measurements of the SnS₂ NW FETs were conducted with the vacuum probe station (Lake Shore TTPX) and semiconductor parameter analyzer (Keithley 4200). The time-resolved response measurements were conducted by periodically turning on and off the laser and monitored by a digital oscilloscope. Noise power spectra measurements were performed by a noise measurement system (NC300).

3 Results and discussion

The SEM image of SnS₂ NWs is displayed in Figure 1a showing a uniform structure with a length of tens of micrometers and a diameter of 30–200 nm. Figure 1b shows the HR-TEM image of single SnS₂ NW and the corresponding SAED pattern indicating that the SnS₂ NW has a single-crystalline architecture at the [001] direction and lattice distance of 0.296 nm. The XRD (Figure 1c) and EDS (Figure 1d) characterizations also conform to the previous studies [25]. The XRD spectrum of SnS₂ NWs shows all main diffraction peaks at $2\theta \sim 15.0^\circ, 28.3^\circ, 32.0^\circ, 42.0^\circ, 50.0^\circ, 52.5^\circ, 58.4^\circ$, corresponding to (001), (100), (101), (102), (110), (111) and (200) planes (JCPDS No. 23-0677). The EDS spectrum of the sample shows that there are elements such as C, O, Cu, Si, S and Sn, in which the presence of C and Cu is caused by the carbon micro gate substrate, while O is the oxide layer of the sample surface.

To investigate the photodetection characteristics of SnS₂ NWs, back-gated SnS₂ NW FETs were fabricated, as shown in Figure 2a. Figure 2b shows the $I_{ds}-V_{gs}$ transfer characteristics of the device for various V_{ds} and the linear $I_{ds}-V_{gs}$ transfer characteristic at $V_{ds} = 1$ V is displayed in Figure 2c. The SnS₂ NW FETs exhibit typical n-type transistor behavior with a high current on-off ratio of 10^7 at $V_{ds} = 1$ V. The electron mobility μ_{FE} of single SnS₂ NW FET is calculated using the expression: $\mu_{FE} = g_m L^2 / (C_g V_{ds})$ [26, 27], where $g_m = dI_{ds}/dV_{gs}$ is the transconductance and C_g is the capacitance of back-gated FET. C_g can be estimated on the basis of the cylinder on-plane model [26]: $C_g = 2\pi\epsilon_0\epsilon_r L / [\ln(4h/d)]$, where ϵ_0 is free space permittivity, ϵ_r is the dielectric constant of SiO₂, $h = 110$ nm is the thickness of the SiO₂ layer, $L = 3$ μ m is the length of the channel, $d = 90$ nm is the diameter of SnS₂ NW (see the inset in Figure 3d). The calculated μ_{FE} is ~ 131.8 cm² V⁻¹ s⁻¹ at $V_{ds} = 1$ V. The electron mobility of the SnS₂ NW FET is comparable with that of

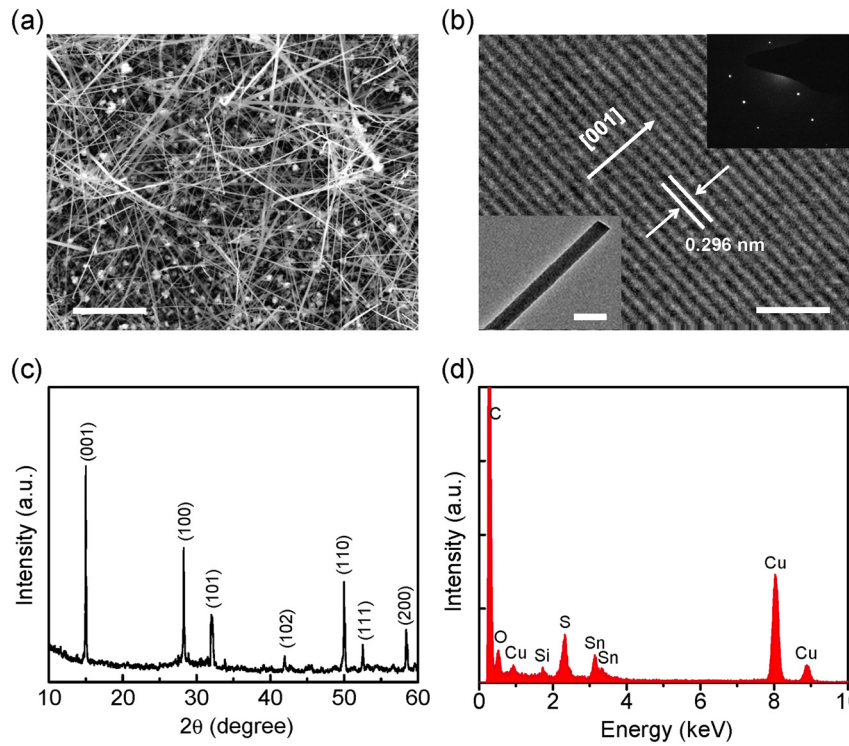


Figure 1: Characterizations of the as-grown SnS_2 NWs. (a) SEM image of SnS_2 NWs. The scale bar is $2\ \mu\text{m}$. (b) HR-TEM image indicates a single-crystalline structure along the $[001]$ growth direction of the single SnS_2 NW. The lattice distance is $\sim 0.296\ \text{nm}$ and the scale bar is $2\ \text{nm}$. The bottom-left inset shows that the diameter of NW is about $40\ \text{nm}$ (the scale bar is $50\ \text{nm}$). The top-right inset is the corresponding SAED pattern. (c) XRD pattern of the SnS_2 NWs (JCPDS No. 23-0677). (d) EDS spectrum of the SnS_2 NWs.

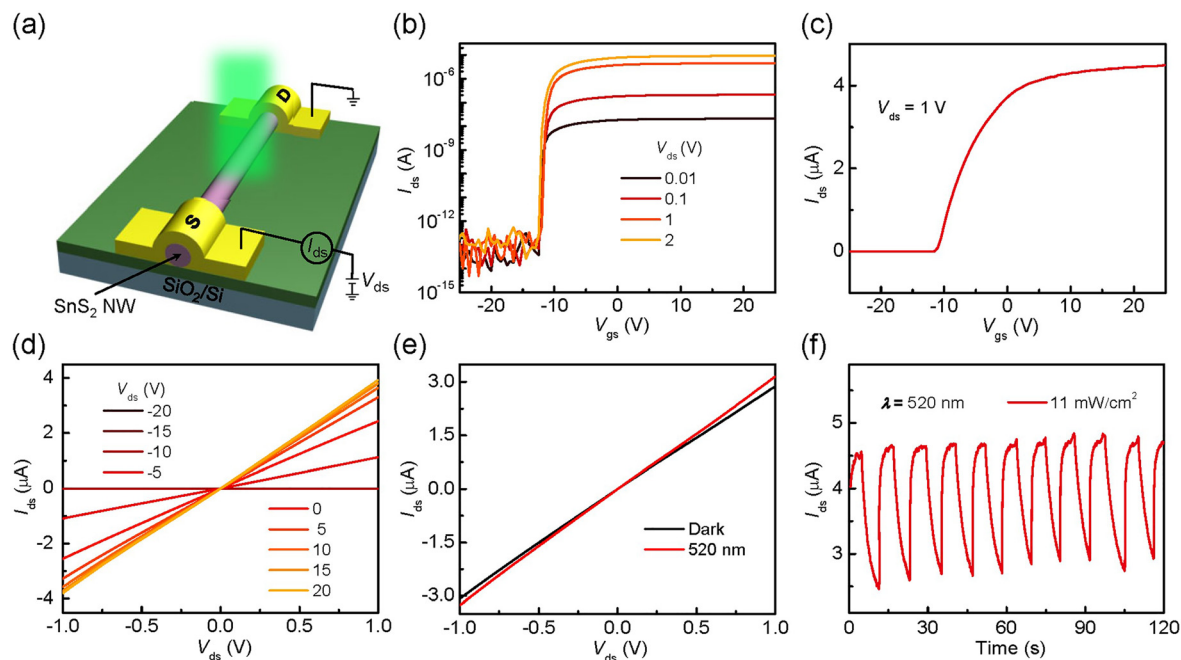


Figure 2: Electrical and optoelectronic properties of the single SnS_2 NW back-gated FET. (a) Schematic diagram of the single SnS_2 NW back-gated FET photodetector. (b) $I_{\text{ds}}-V_{\text{gs}}$ transfer characteristics of the SnS_2 NW FET at different bias voltages V_{ds} . (c) Linear plot of $I_{\text{ds}}-V_{\text{gs}}$ transfer characteristics at $V_{\text{ds}} = 1\ \text{V}$. (d) $I_{\text{ds}}-V_{\text{ds}}$ output characteristics of the device, the back-gated voltage interval is $5\ \text{V}$. (e) $I_{\text{ds}}-V_{\text{ds}}$ output characteristics of the device in dark and under the illumination of $520\ \text{nm}$ laser, measured without additional back-gated voltage. (f) Photocurrent response of the device under optical chopping at $V_{\text{ds}} = 1\ \text{V}$ without additional back-gated voltage ($520\ \text{nm}$, $11\ \text{mW cm}^{-2}$).

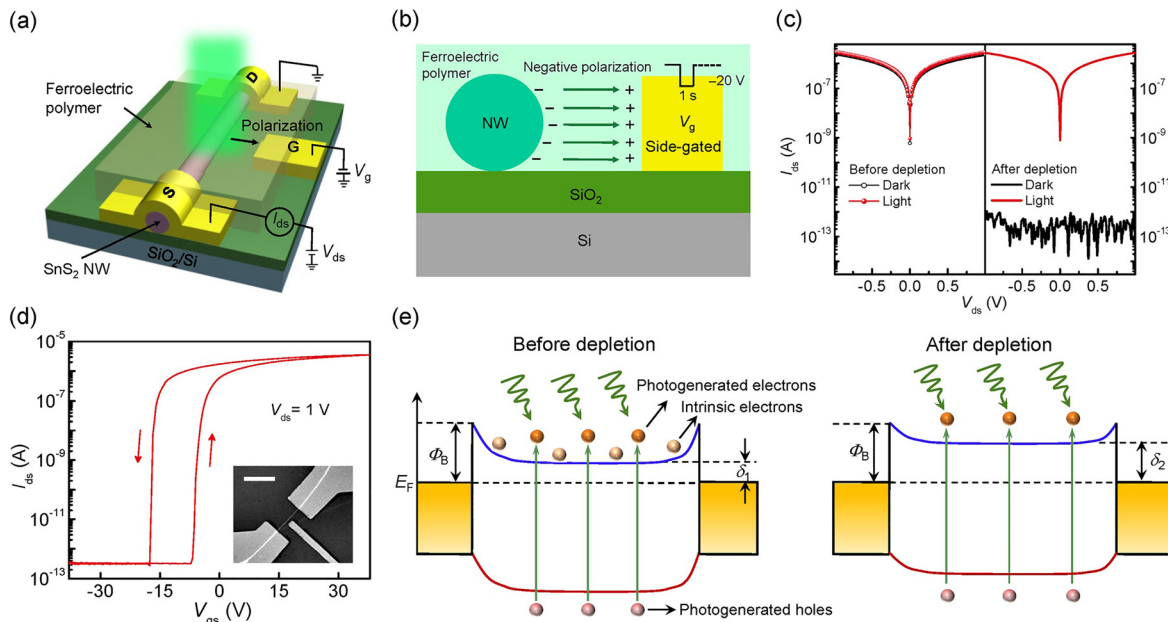


Figure 3: Schematic diagrams, polarization characteristics and energy band diagrams of ferroelectric side-gated single SnS₂ NW photodetector. (a) Schematic illustration of ferroelectric side-gated single SnS₂ NW photodetector. (b) The cross-sectional structure and schematic diagram of the device in negative polarization state. (c) Before and after being depleted by the ferroelectric polymer film, I_{ds} – V_{ds} characteristics of the device in the dark and upon illumination (520 nm, 11 mW cm^{−2}). (d) I_{ds} – V_{gs} transfer curves of the device with local field regulation by P(VDF-TrFE) ferroelectric polymer and side-gated voltage. The inset is the SEM image of the SnS₂ NW side-gated FET. (e) Energy band diagrams before and after depletion. E_C is the minimum conduction band energy, E_V is the maximum valence band energy, E_F is the Fermi level energy, Φ_B is the Schottky barrier height, and δ is the height from bottom of conduction band to the Fermi level energy. The bottom of E_C rises upward and δ increases after being depleted.

other n-type SnS₂ nanodevices [14]. The I_{ds} – V_{ds} output characteristics of SnS₂ FET are shown in Figure 2d. The linear I_{ds} – V_{ds} curves indicate good Ohmic contacts at the metal/SnS₂ interface further demonstrating the n-type semiconducting behavior [13]. The net photocurrent under 520 nm illumination is 0.28 μ A at a $V_{ds} = 1$ V, as shown in Figure 2e. Obviously, the large dark current leads to a low I_{ph}/I_{dark} ratio which deteriorates the detectivity of the photodetectors.

The photoresponse characteristics of SnS₂ NW photodetector without additional back-gated voltage (520 nm, 11 mW cm^{−2}) are displayed in Figure 2f. Due to the surface states and crystal quality, the rise and fall time of SnS₂ NW photodetectors are about 1.1 and 4.3 s [4, 28–31]. Further, the SnS₂ NW photodetector exhibits a relatively low I_{ph}/I_{dark} ratio because of the large dark current. Therefore, to suppress the dark current we employed P(VDF-TrFE) polymer as side-gate, in which the strong local polarization field can fully deplete the carriers suppressing the dark current. The structure diagram of the device is shown in Figure 3a. The SEM image of a representative side-gated SnS₂ NW device is given in the inset of Figure 3d, the distance between the NW and the side-gated electrode is 240 nm. The polarization intensity generated

by ferroelectric polymer films is much higher than that of gate electrostatic field in transistors [31–36]. Figure S1 shows the hysteresis line of P(VDF-TrFE) capacitor, the coercive voltage is about 7.9 V and the remnant polarization value is about 5.2 μ C cm^{−2}, indicating the strong polarization performance of ferroelectric polymer [32]. Therefore, the stable residual polarization induced by negative voltage can largely deplete the carriers. Even the negative gate voltage is removed, the device still maintains the depletion state. Figure S2 shows the retention characteristics of the device after depletion. The device remained in the depleted state for more than 60 000 s, indicating that the device can work stably for a long time without additional negative gate voltage. Figure 3d shows the transfer properties of SnS₂ NW FETs with local field regulation by P(VDF-TrFE) ferroelectric polymer dielectric and side-gated voltage. The linear curve is shown in Figure S3 in the Supplementary Materials. The hysteresis loops along the counterclockwise direction indicate that the ferroelectric local field is well regulated and has strong residual polarization characteristics [31, 32]. Noted that the dark current is suppressed to 3×10^{-13} A for side-gated voltage less than −17.2 V because of the residual polarization of the ferroelectric polymer film.

The negative polarization can deplete the intrinsic carriers in NW. For example, when a negative voltage of -20 V and pulse width of 1 s is introduced to the side-gated electrode, the ferroelectric polymer film is polarized with polarization direction from NW to the side-gated electrode. So, most free electrons in the SnS₂ NW channel were depleted by the ultra-high polarization electrostatic field, as shown in Figure 3a and b. The schematic diagrams of the ferroelectric side-gated SnS₂ nanowire structure before depletion and after depletion are shown in Figure S4 in the Supplementary Materials. After the electrons in the NW are depleted, the conduction band energy level rises upward, and the channel current drops below pA, in a depleted state. The energy band diagram before and after depletion is shown in Figure 3e. Because of the residual polarization in ferroelectric polymer, the dark current is largely suppressed and decreased to below 10^{-12} A (Figure 3c), even after removing the side-gated voltage [34]. With reduced dark current, the performance of NW photodetectors was dramatically improved. For example, the I_{ph}/I_{dark} ratio was improved from less than 1 to 10^7 and the photocurrent was also improved from 0.49 to 2.74 μ A with the introduction of a strong polarization electric field. The mechanism of polarization-manipulated photodetection is shown in Figure 3e. Before depletion, the dark current comes from the thermionic emission and tunneling of the intrinsic carriers. Under illumination, the photo-generated electron-hole pairs contribute to the photocurrent together with the dark current to form the total channel current. With negative polarization, the electrons in the NW are depleted and the conduction band minimum rises upward, then the channel

current under illumination is mainly attributed to the photo-generated electron-hole pairs [6, 34, 35].

Next, the photoresponse of the device with depletion was further characterized. Figure 4a shows the photo-induced I_{ds} - V_{ds} curves of polarization-depleted SnS₂ NW photodetectors. The extracted I_{ph} as a function of light intensity is shown in Figure 4b. The results show that the photocurrent I_{ph} obviously depends on the light intensity (P), which can be expressed as [4] $I_{ph} = cP^k$, where k is 0.7 through the fitting of I_{ph} - P curve. These results indicate complicated processes of photo-generated electron-hole generation, capture, and recombination in the SnS₂ NW device [4, 7].

To evaluate the conversion efficiency and photo-response of the SnS₂ NW photodetector, the key parameters such as photoconductive gain (G), responsivity (R), and detectivity (D^*) are characterized, and their dependence on the light intensity are also studied, as shown in Figures 4c and 5a. The photoconductive gain G of the device is defined as $G = N_e/N_{ph}$, where N_e and N_{ph} , respectively, represent the electron-hole pairs number and the photons number absorbed by NW in unit time [3, 31]. The G can be defined as $G = (I_{ph}/e)/(PA/hv)$ [3, 31], where $h\nu$ is the energy of the incident photon, e is the charge of electron, and A is the effective irradiated region of the photodetector. The effective irradiated region could be estimated through the cross-sectional area of SnS₂ NW, which can be expressed as $A = L \times d$, where L is the length of the NW channel, d is the diameter of the NW [3, 31]. The responsivity or sensitivity is also a key parameter expressing the ability of the photodetector to convert light signals into electrical

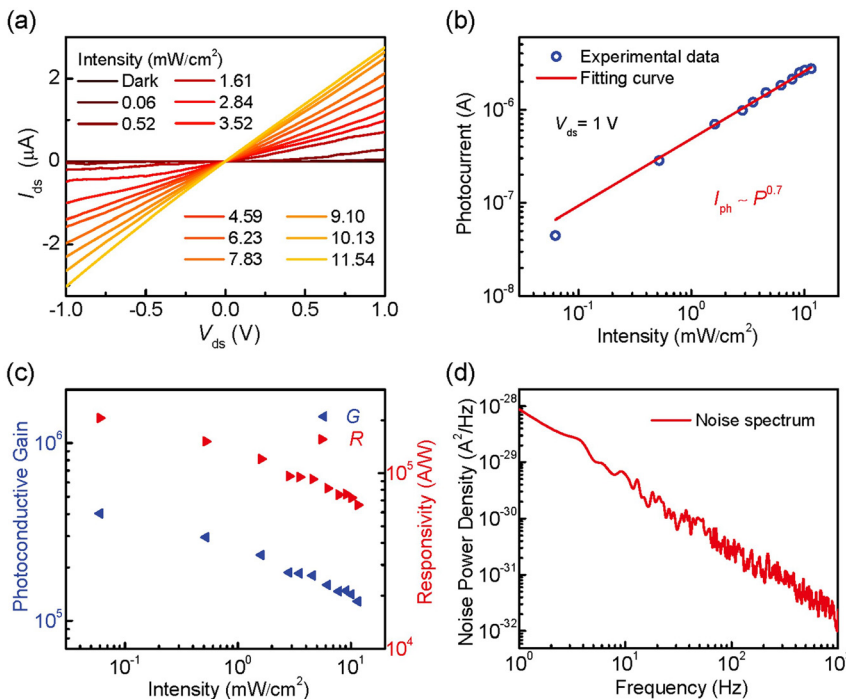


Figure 4: Photoresponse properties and noise power density spectra of ferroelectric side-gated single SnS₂ NW photodetector. (a) I_{ds} - V_{ds} characteristics of the photodetector under various intensities (520 nm) after being depleted, without additional gate voltage. (b) Dependence of photocurrent on various intensities, at $V_{ds} = 1$ V. (c) Dependence of photoconductive gain and responsivity on various intensities, at $V_{ds} = 1$ V. (d) Noise power density spectra of the device at $V_{ds} = 1$ V.

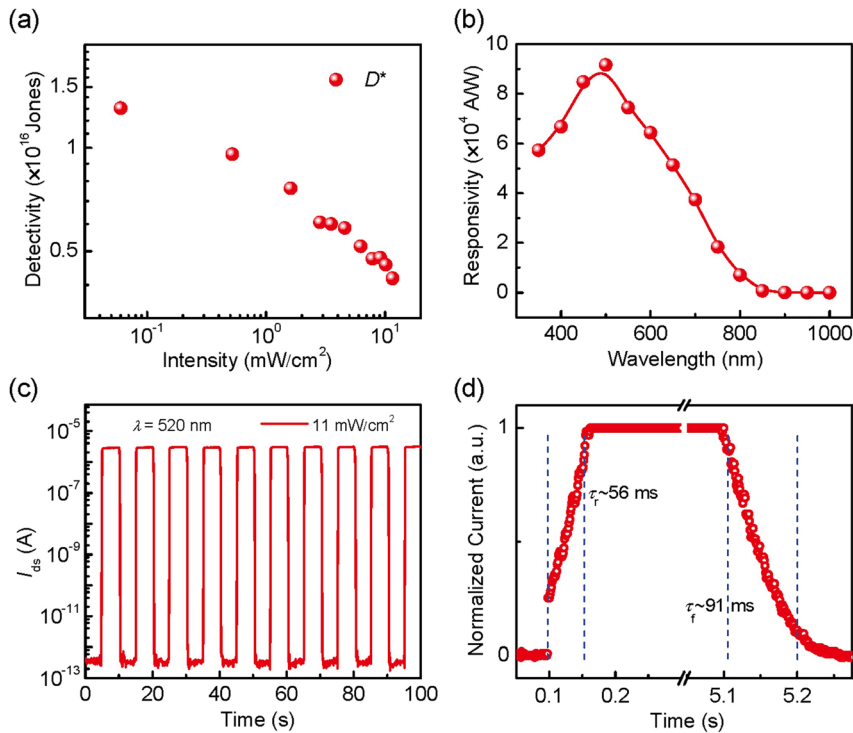


Figure 5: Detectivity, spectral and time response characterizations of ferroelectric side-gated single SnS₂ NW photodetector. (a) Specific detectivity of the photodetector under various intensities, at $V_{ds} = 1 \text{ V}$. (b) Spectral response of the device at different illumination wavelengths from 350 to 1000 nm under light intensity of 3.8 mW cm^{-2} , $V_{ds} = 1 \text{ V}$. (c) Photocurrent response of the device under optical chopping (520 nm, 11 mW cm^{-2}) at $V_{ds} = 1 \text{ V}$, without additional gate voltage. (d) Time-resolved photocurrent response of the device.

signals, which can be expressed as the ratio of the output photocurrent signal of the detector to the incident light power, expressed as $R = I_{ph}/(PA)$ [3, 32, 37]. Figure 4c presents the G and R of the photodetector at various light intensities and $V_{ds} = 1 \text{ V}$. The results show that the G is 4.0×10^5 and R is up to $2.1 \times 10^5 \text{ A W}^{-1}$ under the low light intensity of 0.06 mW cm^{-2} . Obviously, because of the high mobility of the device, the carrier transit time in the channel is short leading to high gain [3, 7]. The specific detectivity represents the minimum detectable signal of the photodetector, which is defined as $D^* = (AB)^{1/2}/(\text{NEP})$ [3, 38], where B and NEP represent the electrical bandwidth and the NEP. NEP is generally related to three main noise sources, namely shot noise, Johnson noise, and flicker noise ($1/f$ noise) [34, 38]. Noise power density spectra of a single SnS₂ NW photodetector was measured by a noise spectrum analyzer at 1 V bias, as shown in Figure 4d. Figure S5 shows the noise power density spectra of the device under different bias voltages. The low-frequency noise characteristics of the device at 0.1 and 1 V bias are close to the background noise at 0 V bias [34]. When the frequency is low, $1/f$ noise plays a dominant role, thermal noise and shot noise are independent of frequency and are not obviously observed, which is mainly because of the device's low dark current [34, 39]. The NEP can be presented as $\text{NEP} = \sqrt{\langle i_n^2 \rangle}/R$ [34, 38, 39], where $\langle i_n^2 \rangle = 6.9 \times 10^{-28} \text{ A}^2$ is the total noise current power,

which is integrated noise power density $S_n(f)$ in the device bandwidth. The flicker noise current of the photodetector $i_n \approx 2.6 \times 10^{-14} \text{ A}$.

Figure 5a shows that the D^* is up to 1.3×10^{16} Jones at a low light intensity of 0.06 mW cm^{-2} . Under the same conditions, D^* with polarization depletion is at least two orders of magnitude higher than that of device without polarization depletion due to the reduced dark current. The SnS₂ NW photodetectors with ultra-high R and D^* show better performance than other photodetectors based on Tin sulfides SnS and SnS₂ nanostructure reported, as shown in Table 1. Noted that the shot noise caused by dark current would be considered as the main factor restricting the device detectivity at a larger frequency range [3]. Therefore, the suppression of dark current is a critical way to improve the detectivity of the device. With ferroelectric polarization to suppress the dark current, the SnS₂ NW photodetectors show an ultrahigh detectivity of 1.3×10^{16} Jones.

The spectral response characteristics of SnS₂ NW photodetector from 350 to 1000 nm are displayed in Figure 5b. Noted that the device still shows photoresponse even beyond the cut-off wavelength of 590 nm ($E_g \sim 2.1 \text{ eV}$). The extended detection wavelength may come from the changes of surface electron-hole distribution and band structures induced by a strong ferroelectric polarization field [32, 37]. Figure S5 shows the photoresponse properties of ferroelectric side-gated SnS₂ NW photodetector after depletion under deep ultraviolet illumination (280 nm).

Table 1: Comparison of the critical parameters for tin sulfides SnS and SnS₂ nanostructure photodetectors.

Photodetectors	I_{on}/I_{off}	Rise time	Fall time	Responsivity ($A W^{-1}$)	Detectivity (Jones)	References
SnS NW	1.5	1.2 ms	15.1 ms	1.6×10^4	2.4×10^{12}	[7]
SnS NB	10^2	36 ms	7 ms	3×10^2	6×10^9	[8]
SnS NS	1.3	5.1 ms	8.8 ms	156	2.94×10^{10}	[9]
SnS ₂ NS	10^6	22 ms	11 ms	100		[12]
SnS ₂ NS	10^3	5 μ s	7 μ s	8.8×10^{-3}	2×10^9	[13]
SnS ₂ NS	10^2	20 ms	16 ms	261	1.9×10^{10}	[18]
SnS ₂ NS	2.3	40 ms	80 ms	1.1×10^3	10^{12}	[40]
SnS ₂ NW	10^7	56 ms	91 ms	2.1×10^5	1.3×10^{16}	This work

NW, nanowire; NB, nanobelt; NS, nanosheet.

The device exhibits ultra-sensitive photoresponse and wider spectrum detection from ultraviolet to near-infrared [32, 36]. The time-resolved photoresponse was conducted under 520 nm illumination, shown in Figure 5c. The SnS₂ NW device exhibits good switching periodicity and stability, and the I_{light}/I_{dark} ratio is as high as 10^7 . To precisely measure the device's rise and fall times, a digital oscilloscope was applied to monitor fast-changing photocurrent signals [41, 42]. Figure 5d shows the time-resolution photocurrent response, and the rise time and fall time are 56 and 91 ms respectively. The rise and fall time are tens of times faster than those without ferroelectric polarization. The faster response and recovery time exhibit the faster generation and recombination of electron–hole pairs, which may be related to ultra-high ferroelectric polarization local field generated by P(VDF-TrFE) ferroelectric polymer [30, 34, 43].

4 Conclusions

In this work, we demonstrated CVD-grown SnS₂ NWs for photodetectors. To reduce the dark current, the ferroelectric side-gate was integrated with SnS₂ NW photodetectors. The dark current is significantly suppressed and minimized after depletion through the ultra-high electrostatic polarization, even after removing the side-gated voltage. The ferroelectric side-gated devices show a large photoconductive gain, ultra-high detectivity and responsivity, rapid response speed, and good spectral response characteristics from ultraviolet to near-infrared. This work opens new avenues for ferroelectric-manipulated photodetection technology.

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